

09/853 111



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:

US 7,008,824 B2

SALMAN AKRAM

Issued: May 10, 2001

For:

Method Of Fabricating Mounted Multiple

Semiconductor Dies In A Package

Atty. Dkt. No.:

Examiner:

Group Art Unit:

MCT.0012D1US

A. Chambliss

2814

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO MISTAKE (37 C.F.R. § 1.322(a))

- Attached, in duplicate, is Form PTO-1050, with at least one copy being suitable for 1. printing.
- The exact page and line number where the error is shown correctly in the application file in the Preliminary Amendment filed October 6, 2005, page 3, claim 32 (renumbered in the printed patent to claim 8), line 7.
 - 3. Please send the Certificate to:

Timothy N. Trop TROP, PRUNER & HU, P.C. 8554 Katy Freeway, Suite 100 Houston, Texas 77024-1805

All errors sought to be corrected were made in printing by the Patent and Trademark 4. Office and no fee is believed to be due.

Respectfully submitted,

Date: March 28, 2006

Timoth N. Trop, Registration No. 28,994

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Date of Deposit: March 28, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box

1450, Alexandria, VA 2231 Sherry Tipton

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: US 7,008,824 B2

DATED: March 7, 2006

INVENTOR(S): Salman Akram

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6:

Line 13, after "leadframe", insert --fingers--.

MAILING ADDRESS OF SENDER (Please do not use customer number

PATENT NO. US 7,008,824 B2

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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DATED: March 7, 2006
INVENTOR(S): Salman Akram

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